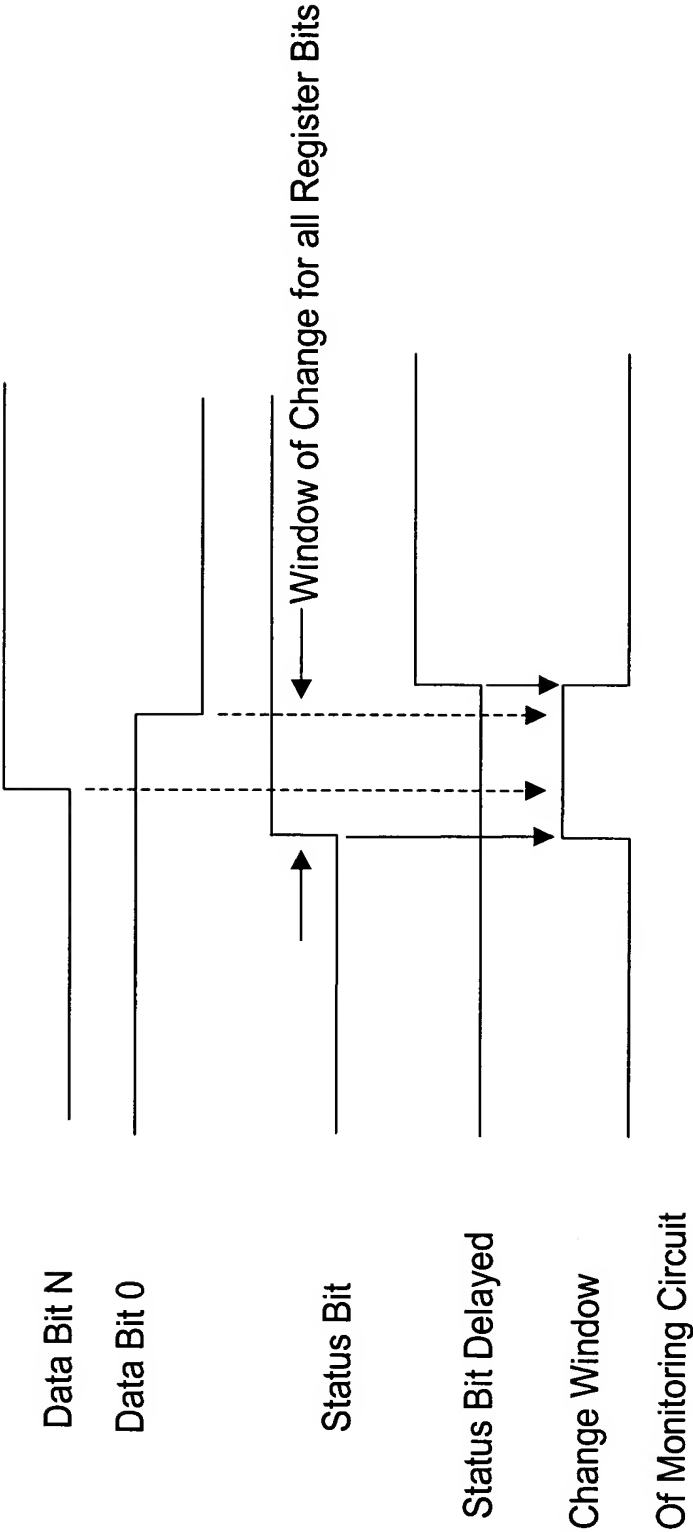


Figure 1



Status bit is the fastest bit, changes first
Data Bit 0 is slowest bit, changes last
Status and data bits are outputs of source register
Change Window sized to cover all bit variations

Figure 2

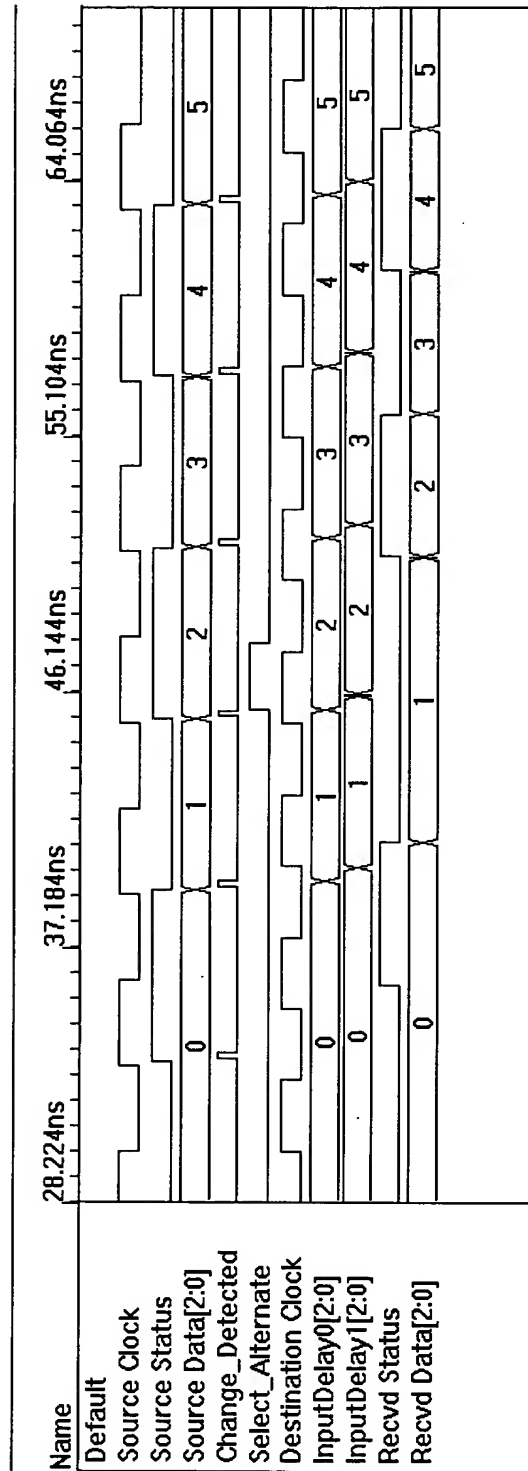


Figure 3

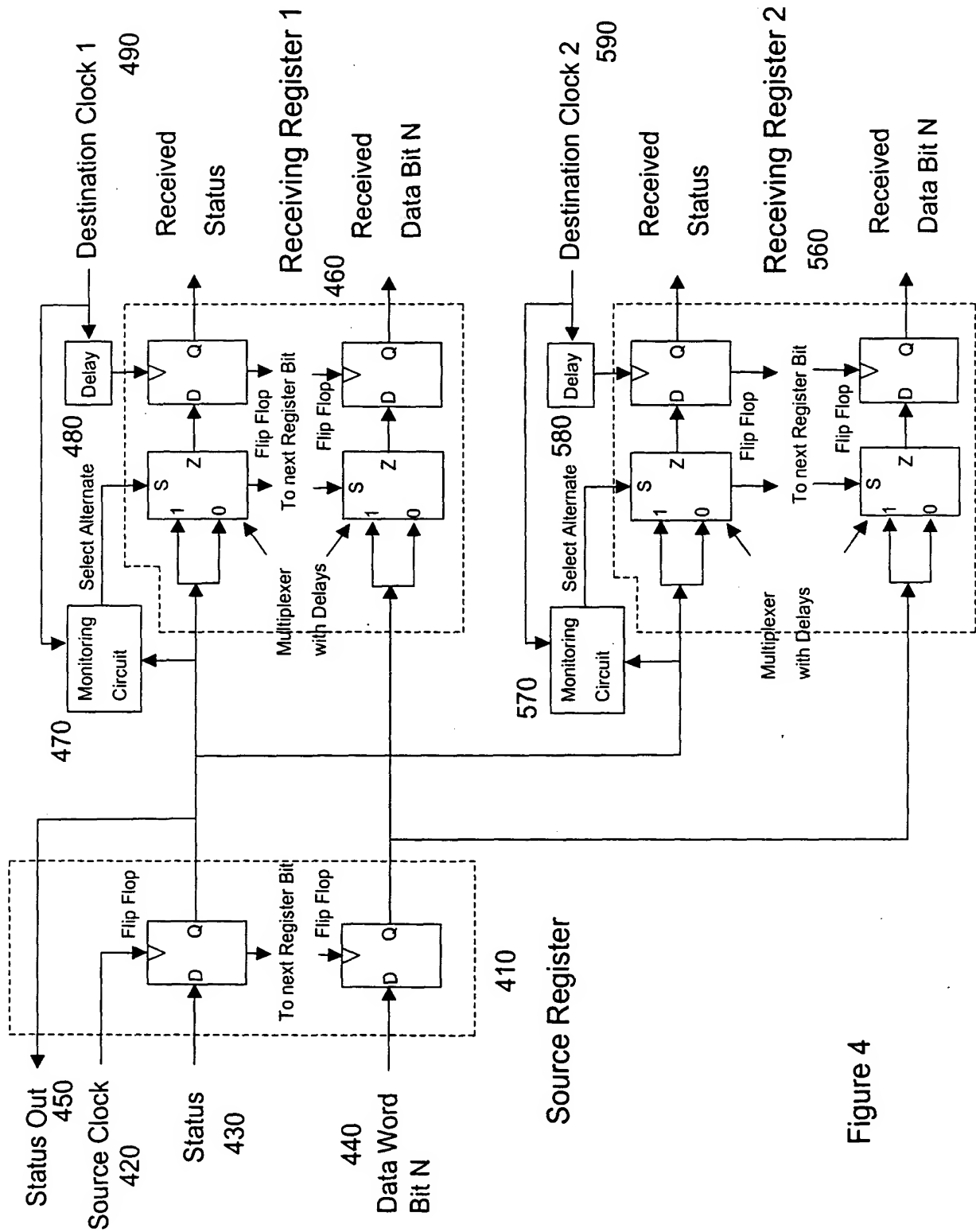


Figure 4

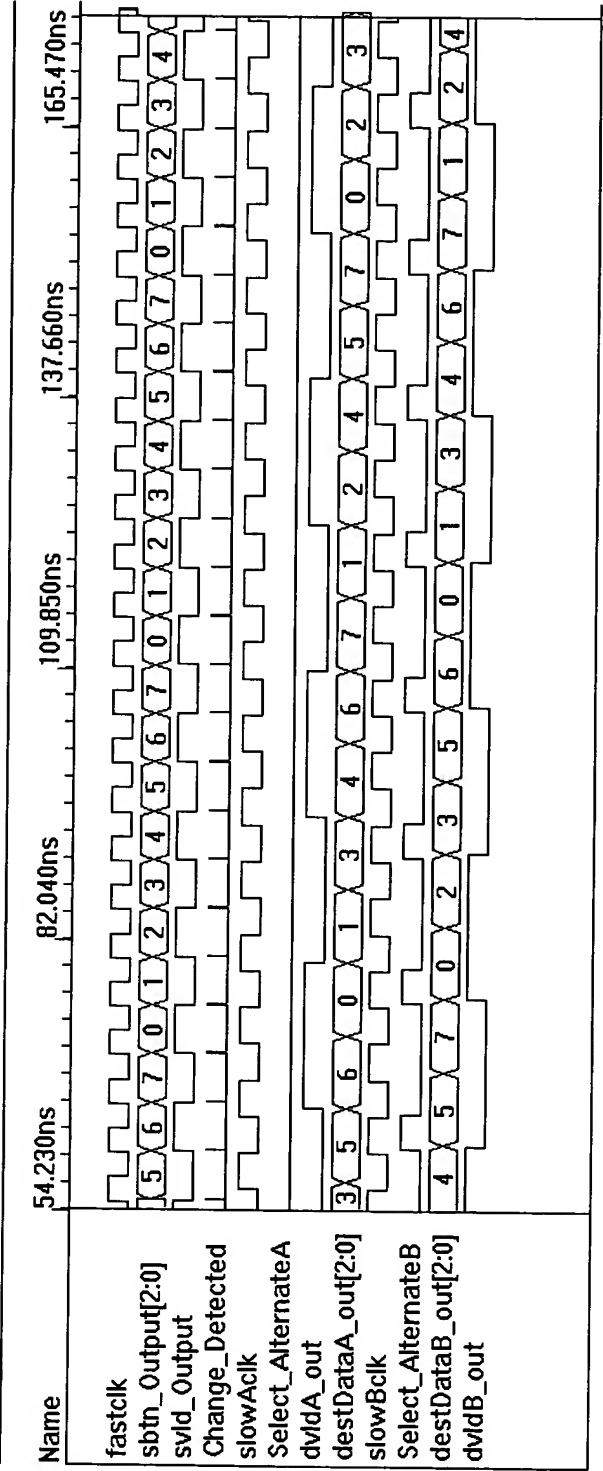


Figure 5